PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re the Application of

Chiharu IRIGUCHI

Group Art Unit: 2862

Application No.:

10/790,221

Examiner:

R. FULLER

Filed: March 2, 2004

Docket No.: 118580

For:

ALIGNER, EXPOSING METHOD, METHOD FOR MANUFACTURING THIN-FILM

TRANSISTOR, DISPLAY DEVICE, AND ELECTRONIC DEVICE USING SHADING

MEANS

SUBMISSION OF REFERENCES UNDER 37 C.F.R. §1.97(i)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.97(i) and MPEP 609.05(a), Applicant requests that the attached reference be placed in the Patent Office files. The reference was cited in a Communication dated March 12, 2008 from the Japanese Patent Office.

Respectfully submitted,

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JAO:CJW/add

Date: April 4, 2008

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